

II. SPECIFICATION AMENDMENTS

Please replace the paragraph beginning on page 9, line 11 through line 37 as rewritten below:

Figure 5 shows as a block diagram the description of a device according to the invention for timing the processing of data packets. The device comprises a telecommunication connection 51 to an IP-based data network 12, which telecommunication connection can be implemented, for example, through a public telephone network and a gateway, directly through a local network or through a wireless connection to the data network. Along the telecommunication connection 51, information is transferred to the device in data packets 45 that arrive inside data frames 41. The information arriving in the data packets is received in a Master Controlling Unit 52 (MCU), which is, for example, a microprocessor. The Master Controlling Unit is arranged, on the basis of a program stored in a memory 53, to store the data packet or data packets that arrived in the data frame in the memory 53. Lower down, the figure shows the physical block diagram of the Master Controlling Unit 52 and father up, the figure shows inside a dashed line the functional block diagram of the Master Controlling Unit 52. The Master Controlling Unit further comprises a clock 54, example, the clock of a microprocessor for determining the course of time and functions implemented with programs stored in the memory 53 for calculating 55 the delay of a data packet from the difference between the time of transmission and the time of arrival and for calculating 56 a play-out delay on the ~~based~~ basis of the delay values of the last n data packets. The Master Controlling Unit is arranged, on the basis of a program stored in the memory 53, to unpack the

data frame with a decoder application on the basis of a response obtained from the clock. In addition, a transfer function 57 is stored in the memory for transferring data packets to the Master Controlling Unit for being processed and for being presented to the user by a player 38 on the basis of a response obtained from the clock of the reaching of a specific time limit.